

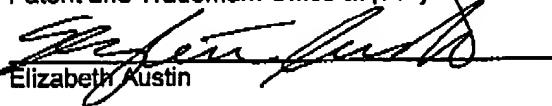
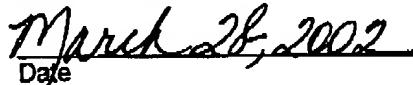
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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**In re the Application of: **Gerard Chauvel, et al.**Docket Number: **TIF-15767A**Serial No.: **08/890,894**Art Unit: **2186**Filed: **07/10/1997**Examiner: **Tran, D.**For: **MULTIPLE PROCESSOR APPARATUS HAVING A PROTOCOL PROCESSOR INTENDED FOR THE EXECUTION OF A COLLECTION OF INSTRUCTIONS IN A REDUCED NUMBER OF OPERATIONS****CERTIFICATION OF FACSIMILE TRANSMISSION**

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NAME OF INVENTOR(S): <b>Gerard Chauvel, et al.</b>	
RECEIPT DATE & SERIAL NO.: <b>08/890,894</b>	
FILING DATE: <b>07/10/1997</b>	
TITLE OF INVENTION: <b>Multiple Processor Apparatus Having a Protocol Processor Intended for the Execution of a Collection of Instructions in a Reduced Number of Operations</b>	
TI FILE NO.: <b>TIF-15767A</b> DEPOSIT ACCT. NO.: <b>20-0668</b>	
DATE FAXED: <b>March 28, 2002</b>	
DUE: <b>February 28, 2002</b>	
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